RESPONSE UNDER 37 C.F.R. § 1.116 **EXPEDITED PROCEDURE EXAMINING GROUP 2800** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

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Egerer

Docket No.:

54382-20048.00

(QIM 2003 P 50294 US)

Serial No.:

10/812,395

Art Unit:

2836

Filed:

March 30, 2004

Examiner:

entued poly RCE Lifed 10/9/01 Sams 10/15/07 Rutland Wallis, Michael

For:

Semiconductor Device Voltage Supply for a System with at Least Two,

Especially Stacked, Semiconductor Devices

Mail Stop: AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RUTLAND-WALLIS

(09/14/2007)

AMENDMET UNDER 37 CFR 8 1.11

Dear Sir:

Applicant respectfully submits the following amendments and remarks in response to Examiner's Office Action dated July 10, 2007, which Action is made final. Applicant respectfully requests that these amendments and remarks be entered in pursuant to the provisions of 37 CFR § 1.116, and respectfully request reconsideration of the claims.

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